## (19) World Intellectual Property Organization International Bureau



## 

(43) International Publication Date 1 July 2004 (01.07.2004)

PCT

## (10) International Publication Number WO 2004/055997 A1

- (51) International Patent Classification7: H04B 1/16, 1/10
- (21) International Application Number:

PCT/IB2003/005215

(22) International Filing Date:

17 November 2003 (17.11.2003)

(25) Filing Language:

English

(26) Publication Language:

English

(30) Priority Data:

02080310.2

16 December 2002 (16.12.2002) E

- (71) Applicant (for all designated States except US): KONIN-KLIJKE PHILIPS ELECTRONICS N.V. [NL/NL]; Groenewoudseweg 1, NL-5621 BA Eindhoven (NL).
- (72) Inventors; and
- (75) Inventors/Applicants (for US only): SESSINK, Franciscus, J., A., M. [NL/NL]; c/o Ekkersrijt 5228, NL-5692 EG Son en Breugel (NL). VAN DEN HEUVEL, Henricus, J., M. [NL/NL]; c/o Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL).

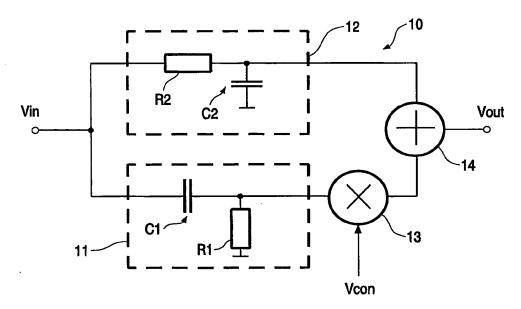
- (74) Agent: GROENENDAAL, Antonius, W., M.; Philips Intellectual Property & Standards, Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL).
- (81) Designated States (national): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.
- (84) Designated States (regional): ARIPO patent (BW, GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

## Published:

with international search report

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: NOISE SUPPRESSION IN AN FM RECEIVER



(57) Abstract: A filter circuit for suppressing high frequency noise in an FM-receiver has a first filter branch and a second filter branch connected in parallel. The first filter branch has a high pass filter (11) and a multiplier (13) for multiplying a signal passing through the first filter branch and a control signal indicative of high frequency noise. The second filter branch has a low pass filter (12).

O 2004/055997 A1